

Roll No.

Total No. of Pages : 02

Total No. of Questions : 07

BCA (Sem.-3rd) (2007 to 10 Batch)
COMPUTER SYSTEM ARCHITECTURE
Subject Code : BC-403
Paper ID : [B0226]

Time : 3 Hrs.

Max. Marks : 60

INSTRUCTION TO CANDIDATES :

1. **SECTION-A** is **COMPULSORY** consisting of **TEN** questions carrying **TWO** marks each.
2. **SECTION-B** contains **SIX** questions carrying **TEN** marks each and students has to attempt any **FOUR** questions.

SECTION-A

1. Write briefly :
 - a) What is meant by operation code?
 - b) What are Computer registers? List various types of computer registers.
 - c) Define Register reference instruction.
 - d) What is instruction cycle?
 - e) List the different addressing modes.
 - f) What is Cache memory? Explain.
 - g) Define the term computer organization and computer architecture.
 - h) What is the role of I/O ports?
 - i) How memory mapping is carried out?
 - j) What is timing diagram? Explain with an example.

SECTION-B

2. What are the different types of computer registers? Discuss their functions.
3. What is control unit? Explain its functions. Explain how micro Programmed control unit is different from hardwired control unit.
4. How transfer of information between CPU and I/O devices is carried out? Explain..
5. What is microprocessor? Explain the architecture of 8-bit microprocessor.
6. Write a note on :
 - (a) Auxiliary memory
 - (b) Associative memory
 - (c) Memory mapping
7. Write short notes on the following :
 - (a) Instruction Cycle
 - (b) I/O Architecture

a2zpapers.com